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L2	139885	(clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 14:53
L3	2	(clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))with (((reduc\$4 or lower or minimiz\$4)near3 power)near3 UART)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 14:58
L4	1	((selecti\$4 or adapt\$5)near4 (switch\$4 or chang\$4 or lower\$4))near6 (mode or state))with uart	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 14:47
L5	3	(clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))same (((reduc\$4 or lower or minimiz\$4)near3 power)near3 UART)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:32
L6	1499	l2 and UART	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 14:54
L7	92250	(communicati\$4 or transmi\$4 or receiv\$4)same ((low or reduc\$4 or sleep or inactive)near2 (power or state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 14:57
L8	459	l6 and ((communicati\$4 or transmi\$4 or receiv\$4)same ((low or reduc\$4 or sleep or inactive)near2 (power or state)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:00

L9	459	l8 and (clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 14:58
L10	716	l6 and (serial adj data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:00
L11	459	l8 and ((communicati\$4 or transmi\$4 or receiv\$4)same ((low or reduc\$4 or sleep or inactive)near2 (power or state)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:01
L12	114	l11 and (interface adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:08
L13	289	((selecti\$4 or adapt\$5)near5 (switch\$4 or select\$4 or chang\$4 or alter\$5))with ((serial or parallel)near4 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:11
L14	10	l13 same UART	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:14
L15	77	((low or reduced or minimiz\$4)adj3 (state or power or mode))with UART	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:24
L16	22572	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:29

L17	92	((low or reduced or minimiz\$4 or inactive or idle)adj3 (state or power or mode))with UART	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:24
L18	18	l16 and (((low or reduced or minimiz\$4 or inactive or idle)adj3 (state or power or mode))with UART)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:30
L19	104177	"327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:29
L20	56641	"375"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:30
L21	2	l19 and (((low or reduced or minimiz\$4 or inactive or idle)adj3 (state or power or mode))with UART)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:31
L22	9	l20 and (((low or reduced or minimiz\$4 or inactive or idle)adj3 (state or power or mode))with UART)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:30
L23	2	L16 and ((clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))same (((reduc\$4 or lower or minimiz\$4)near3 power)near3 UART))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:35
L24	1	L19 and ((clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))same (((reduc\$4 or lower or minimiz\$4)near3 power)near3 UART))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:34

L25	1	L20 and ((clock near3 (control\$4 or switch\$4 or chang\$4 or reduced or lower))same (((reduc\$4 or lower or minimiz\$4)near3 power)near3 UART))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:34
L26	13	((uart with ((parallel or srial)near2 interface))same clock\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:39
L27	16	((uart same ((parallel or srial)near2 interface))same clock\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:41
L28	1171	((uart and ((clock or frequenc\$4 or speed) near4 (control\$4 or adjust\$4 or selecti\$4 or switch\$3 or adapt\$4))))and (power near3 (low\$4 or reduc\$5 or optimiz\$4 or minimiz\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:46
L29	210	((uart same ((clock or frequenc\$4 or speed) near4 (control\$4 or adjust\$4 or selecti\$4 or switch\$3 or adapt\$4))))and (power near3 (low\$4 or reduc\$5 or optimiz\$4 or minimiz\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:48
L30	2	I29 and (parallel with ((one or first)adj3 (clock or rate or speed)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:52
L31	4	I29 and (serial with ((other or second)adj3 (clock or rate or speed)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:53
L32	79	I29 and (serial adj2 communicati\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:54

L33	9	I32 and (parallel adj2 communicati\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 15:55
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An in-band power-saving protocol for mobile data networks

Salkintzis, A.K. Chamzas, C.

Dept. of Comput. Eng., Democritus Univ. of Thrace, Xanthi, Greece;

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Volume: 46 , Issue: 9

ISSN: 0090-6778

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Inspec Accession Number: 6030061

Abstract:

A new alternative is proposed for **reducing** the **power** consumption of the po (battery-**powered**) units operating in a mobile packet-data network. First, a r the current **power-saving** protocols is given. It is shown that the most comm for conserving **power** is the intermittent operation of the receivers (at the por and a central administration authority that synchronizes the receivers. Some d of the synchronous operation lead us to the introduction of an asynchronous **p saving** protocol, where no central synchronization is necessary and where ea may control its **power** consumption relative to its current needs. According to proposed **power-saving** page-and-answer protocol, an acknowledgment pag procedure is preceding every packet transmission in order to alert mobile term pending traffic. Steady-state performance is evaluated with the aid of simulat relationship between the achieved **power-saving** and the mean packet delay degradation is presented. Finally, we express some notable implementation is some considerations regarding the employment of this protocol as a suppleme **power-saving** service in microcellular mobile data networks and wireless loca networks

Index Terms:

cellular radio data communication delays land mobile radio packet radio networks communication **power** consumption **power** control protocols radio receivers teleco control telecommunication traffic wireless LAN acknowledgment paging procedure a **power-saving** protocol battery-**powered** units central administration authority in-ban

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Dick, R.P.; Lakshminarayana, G.; Raghunathan, A.; Jha, N.K.;

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4 An evolutionary approach to configuring an embedded system based power consumption

Northern, J., III; Shanblatt, M.;

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Glossner, J.; Iancu, D.; Jin Lu; Hokenek, E.; Moudgill, M.;

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Soo-Hwan Choi; Byung-Kug Kim; Jinwoo Park; Chul-Hee Kang; Doo-Seop Eom
Consumer Electronics, IEEE Transactions on , Volume: 50 , Issue: 1 , Feb. 200
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7 A 600-MHz single-chip multiprocessor with 4.8-GB/s internal shared pipelined bus and 512-kB internal memory

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8 Go reconfigure [programmable logic in handheld devices]

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Inoue, M.; Higuma, T.; Ito, Y.; Kushiro, N.; Kubota, H.;

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10 Stress monitoring using a distributed wireless intelligent sensor sys

*Jovanov, E.; O'Donnell Lords, A.; Raskovic, D.; Cox, P.G.; Adhami, R.; Andras
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11 Applicati ns of asynchronous circuits

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15 Platform tuning for embedded systems design

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